

No. of Printed Pages : 3

MIP-103

**POST GRADUATE DIPLOMA IN
INTELLECTUAL PROPERTY
RIGHTS (PGDIPR)**

Term-End Examination

June, 2025

**MIP-103 : INDUSTRIAL DESIGNS AND LAYOUT
DESIGNS OF INTEGRATED CIRCUITS AND
UTILITY MODELS**

Time : 3 Hours

Maximum Marks : 100

Note : *This question paper is divided into three
Parts : Part A, Part B and Part C. All
Parts are compulsory.*

Part—A

Note : *Explain any **four** of the following in
about **150** words each. Each question
carries 5 marks.*

4×5=20

1. Definition of design.

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2. Piracy of a design.
3. 'Integrated Circuit' (IC).
4. Utility model protection in China.
5. Semiconductor Integrated Circuit under the SICLD Act, 2000.

Part—B

Note : Answer any *five* of the following questions in about **300** words each. Each question carries 10 marks. $5 \times 10 = 50$

6. What is meant by infringement of layout design ?
7. Discuss the impact of infringement of designs on the rights holder.
8. Differentiate between Utility Model and Patent.
9. Explain the definition of 'artistic work' under the Copyright Act, 1957.
10. Distinguish between Industrial Design and Patent.
11. Describe the procedure for the registration of Industrial Design. Also explain the guidelines for the preparation of representation sheet.

12. What is Utility Model ? Discuss the benefits of the rights acquired under the Utility Model Protection.

Part—C

Note : Answer any **two** of the following questions in about **500** words each. Each question carries 15 marks. $2 \times 15 = 30$

13. Discuss the right to prevent unauthorised copying or imitation. Also discuss the right to prevent unauthorised commercial transactions.
14. Discuss the provisions of the Designs Act related to restoration of lapsed design.
15. What is Chip Piracy ? Explain with the help of case laws.
16. Discuss the various International Conventions relating to protection of Utility Model.

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